



Implementers Seminar

Jointly hosted by ANGA, DVB, and ReDeSign

Tuesday 08 September 2009, 10:00 h – 20:00 h CET

IFA TecWatch, Hall 5.3, Messe Berlin, Germany

Agenda

10:00 **Registration & welcome coffee**

10:30h Welcome & introduction by the Chairman

Dr. Peter Siebert,
DVB Managing Director

Session 1: Commercial requirements

10:50 h 1.1 Commercial requirements

Bart Brusse, ReDeSign

Session 2: Technical implementation

11:10 h 2.1 Introduction and architecture

Christoph Schaaf, Kabel Deutschland

11:30 h 2.2 Input processing

Frank Herrmann, Panasonic

11:50 h 2.3 FEC processing, bit-interleaving & mapping

Marten Kabutz, Thomson

12:30 h Lunch

13:30 h 2.4 PLPs and data slices

Jörg Robert, TU Braunschweig

13:50 h 2.5 Time interleaving

Woo Suk Ko, LG

14:10 h	2.6	Frequency interleaving	Sam Atungsiri, Sony
14:30 h	2.7	Usage of pilots	Taku Yamagata, Imagination
14:50 h		Coffee & tea	
15:10 h	2.8	C2-framing	Lothar Stadelmeier, Sony
15:30 h	2.9	L1 & L2 signalling & preamble structure	Sang-Chul Moon, LG
15:50 h	2.10	Preamble signal protection mechanisms	Seho Myung, Samsung
16:10 h	2.11	OFDM generation, RF characteristics, receiver implementation	Michael Heisenberg, Kathrein
16:30 h		Coffee	

Session 3: System aspects

16:50 h	3.1	Simulation and validation	Philipp Hasse, TU Braunschweig
17:10 h	3.2	System aspects & performance	Dirk Jaeger, TU Braunschweig
17:30 h	3.3	DVB-C2 head-end architectures	Ernst Freese, Blankom
17:50 h	3.5	DVB-C2 implementation scenarios	Peter Flötgen, Unitymedia
18:10 h		Discussion and workshop summary by the Chairman	
19:00 h		Implementers networking dinner	
20:00 h		Farewell	

The hosting organizations thank the European Commission for the cofounding of the ReDeSign project (www.ict-redesign.eu) helping to facilitate the execution of the workshop.